

Akros Silicon to Present "Tripling the Power of PoE" Industry Session at APEC 2014

Increases in power delivery beyond limits of current Power-over-Ethernet standards is creating opportunities and challenges for power supply designers and systems integrators

SUNNYVALE, Calif. – February 2, 2014 – <u>Akros Silicon Inc.</u>, a premier supplier of leading-edge intelligent energy management ICs, announces its participation in APEC 2014's <u>Industry Sessions</u> conference program with a presentation titled: "Tripling the Power of PoE." The presenter, Faisal Ahmad, Akros Silicon's VP Marketing, will offer the audience an informative discussion on the future PoE and how it is opening new opportunities and challenges for power supply designers and systems integrators. Ahmad's presentation (IS3.3) is scheduled to begin at 3:30 p.m. on Wednesday afternoon, March 19, 2014, at the Fort Worth Convention Center.

The Akros session will consist of a 20-minute slide presentation, followed by a brief Q&A session, and will cover the following topics:

- PoE Overview
- 4 Pair PoE Power Delivery
- Network Power Management
- Delivering 90W in PoE Systems
- Energy Conservation with PoE

APEC's Industry Sessions were first introduced in 2004 and have grown in popularity every year since. These presentations, solicited from leaders in the power electronics industry, are geared to address topics of current interest; particularly the impact of emerging technologies on today's designs. Unlike APEC's General Session papers, Industry Sessions presentations are not published in the Conference Proceedings and will only be made available to session attendees and upon request following the conference.

For more information, please visit http://www.apec-conf.org/conference/industry-sessions/

Tags: #APEC, # APEC 2014, #PoE, #Akros Silicon, #Akros, #Energy Management, #Power over Ethernet, #PoE+, #PoE++

About Akros Silicon

Headquartered in Silicon Valley, <u>Akros Silicon</u> provides leading edge, intelligent energy management ICs. Akros' disruptive silicon technology makes it possible to integrate multiple ICs, high-voltage isolation and discrete components into a single device, thus enabling electronic OEMs to develop cost-effective and energy-efficient solutions.

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